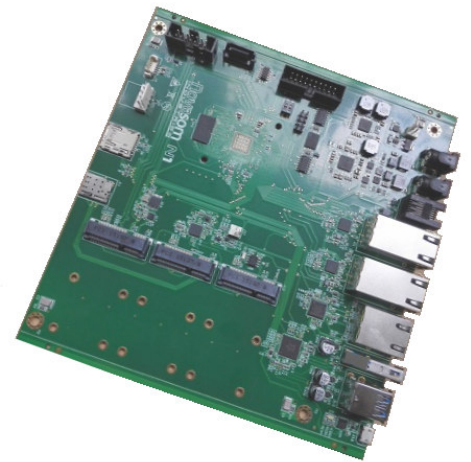












The **Novasom N-line** has been developed for the **ICT** (Information, Communication Technology) and **Telecommunication** markets.

The board based on the **Layerscape NXP** technology is able to **drive a huge amount of TCP/IP data** through the port of the CPU (Ethernet - Pciexpress - SATA III) without loading the CPU by using a dedicated HW Unit.

The board has embedded a **complete UPS** with a **LIPO Battery** and all kernel software managed, allowing the system to shutdown correctly



		SBC-N1C	Option available / NOTE
 Processor	CPU	NXP LS1012 - Single Cortex A53	@ 800Mhz with Layerscape IP
 Memory	RAM memory DDR3 at 16bit	512 MB	Option 256 MB or 1GB
	FLASH	64 Mb QSPI	Option 16 or 32 Mb
	µSD slot	Yes , metal	Any uSD supported
 Power	Power input	6,5-18V protected (up to 22)	Redundant , kernel level managed Require 12V for SATA disk
	UPS manager	Y	
	External battery for UPS	Available as OPT	LIPO 2S any size
	Power Consumption [W]	3	
 Multimedia	Video Output	SPI video out	5 frame per second, vga
	On Board GPIO @ 3,3 V on strip	4	2 shared w SPI
 USB	USB port Host/Device on TYPE - A	3 USB 3.0 + 1 USB 2.0	
	USB Console micro USB .	1 USB 2.0	
	USB on strip	1 USB 2.0	
 Networking	RJ45 Ethernet connector	2	Giga, different MAC
 Generic	Additional nr. 2 user led	Y	
	RTC with ext rechargeable Battery with charger	Y	
	mpci slot full featured NO SIM	1	
	mpci slot only USB 2.0 (1 with SIM)	2	
 Operating temperature	Temperature	-40+ 85	
 Dimension	Mechanical size	170 x 170 mm	
	Form factor	Mini ITX	
 Operating System	Distributions supported	Any Linux, Open WRT	